



FOR IMMEDIATE RELEASE

Ukalta Engineering Announces Ultra-Compact AWGN IP Library

Edmonton, Canada – December 22, 2009 – Ukalta Engineering today announced the commercial availability of its Gaussian noise generator Intellectual Property (IP) cores. With the smallest footprint, best accuracy and highest throughput currently available on the market these IP cores provide an ideal solution for bit-error rate testing of communication systems over additive white Gaussian noise (AWGN) channels.

The AWGN IP is offered in several versions, trading off compactness for probability density function (PDF) accuracy. For instance, the 7.1 sigma AWGN IP core (UGNG-71) exhibits a PDF that deviates less than 0.2% up to 7.1 standard deviations and generates over 400 million Gaussian samples per second using fewer than 1% of the available logic resources on contemporary FPGAs.

Availability

The AWGN IP cores are available immediately as device-independent Verilog or VHDL source code. For further information visit our website at www.ukalta.com or contact Ukalta Engineering at contact@ukalta.com

About Ukalta Engineering

Ukalta Engineering is a Canadian company focused on bringing a multidisciplinary approach to solving problems in the wireless test equipment domain. With seasoned ASIC and FPGA design experience and advanced knowledge of wireless communication systems, Ukalta is strategically positioned as an engineering solutions provider in the wireless industry. Ukalta Engineering endeavours to improve the design and development cycles of wireless products of its customers through the provision of innovative and cost-effective products for channel emulation. For more information, please visit our website at www.ukalta.com

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